

Applicant Initiated Interview Request Form

Application No.: 10/783,491 First Named Applicant: Jack P. Shidemantle
Examiner: Jagan, Mirellys Art Unit: 2855 Status of Application: Non-final Rejection

Tentative Participants:

(1) Examiner Mirellys Jagan (2) Attorney Edward J. Howard
(3) _____ (4) _____

Proposed Date of Interview: 08/08/2008 Proposed Time: 3:00 PM AM/PM

Type of Interview Requested:

(1) ☒ Telephonic (2) ☐ Personal (3) ☐ Video Conference

Exhibit To Be Shown or Demonstrated: ☐ YES ☒ NO

If yes, provide brief description: _____

Issues To Be Discussed

Issues (Rej., Obj., etc)	Claims/ Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1) <u>102(b) Rejection</u>	<u>Claims 8, 16, 25</u>	<u>McGivern</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(2) <u>102(b) Rejection</u>	<u>Claims 27, 30, 32</u>	<u>McGivern</u>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(3) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
(4) _____	_____	_____	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

☐ Continuation Sheet Attached

Brief Description of Argument to be Presented:

See Attached Page

An interview was conducted on the above-identified application on _____.

NOTE: This form should be completed by applicant and submitted to the examiner in advance of the interview (see MPEP § 713.01).

This application will not be delayed from issue because of applicant's failure to submit a written record of this interview. Therefore, applicant is advised to file a statement of the substance of this interview (37 CFR 1.133(b)) as soon as possible.

Edward J. Howard
Applicant/Applicant's Representative Signature

Edward J. Howard

Typed/Printed Name of Applicant or Representative

42,670

Registration Number, if applicable

Examiner/SPE Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/783,491 Applicant : Shidemantle, Jack P.

Confirmation No : 2161 Art Unit : 2859

Filed : 02/20/2004 Atty. Docket: 650271-132
(MSI-YSI-1-US)

Examiner : Jagan, Mirellys

Title : DIGITALLY MODIFIED RESISTIVE OUTPUT FOR A
TEMPERATURE SENSOR

August 8, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AGENDA ITEMS FOR EXAMINER INTERVIEW
FOR DISCUSSION PURPOSES ONLY

1. Claim 8 recites an interface for a monitor and a temperature probe which includes a temperature sensor. Other independent claims recite various other features relating to the structure associated with a temperature probe and method of modifying a temperature sensed from the temperature probe.
2. The cited reference McGivern discloses an electronic circuit which replaces a temperature probe by simulating the resistance characteristics of a temperature sensor. (Col. 2, lines 6—9). In McGivern, the electronic circuit disclosed is a simulation circuit that provides a wide range of resistance values at its output.
3. McGivern simply discloses a means such as a microprocessor which provides a control voltage signal. (Col. 3, lines 50—52). McGivern does not disclose any means which is connected to an actual temperature sensor and which

determines a modified resistive output based on the output of the temperature sensor.

4. In McGivern, the control voltage signal is not dependent on any actual output of a temperature sensor; rather, the control voltage signal is generated randomly between a pre-determined range, e.g. 0 VDC to 10 VDC.
5. McGivern does not disclose the use of any predictive or correlative algorithm. In fact, McGivern does not need any predictive or correlative algorithm because the output of its circuit does not depend on any measured temperature by a temperature sensor.